



Figure 1. Logic Automation Module
图 1. 逻辑控制模块

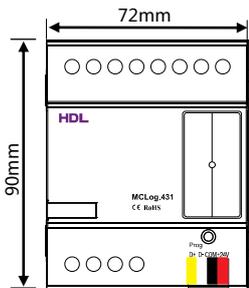


Figure 2. Dimensions - Front View
图 2. 尺寸图 - 正面

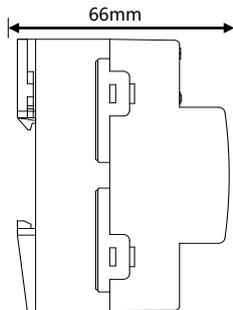


Figure 3. Dimensions - Side View
图 3. 尺寸图 - 侧面

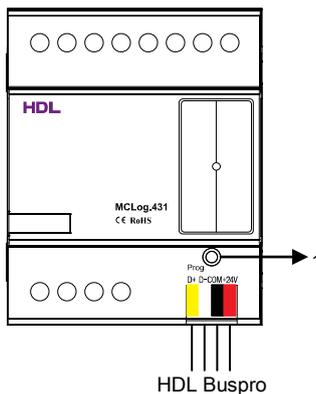


Figure 4. Components - Front View
图 4. 部件 - 正面图

Overview

Logic Automation Module (See Figure 1) is used for controlling over a series of targets. Totally 12 logic groups, 240 logic blocks can be set. The logic conditions enable date and timing information input, universal switch status, and external inputs.

Functions

- Event settings for each day.
- Up to 12 logic groups can be set, each logic group has 20 logic blocks. Logic block can be connected with another one, which means the output pin of one logic block can be the input pin of another logic block.
- Each logic block has 4 input Pins and 20 output control targets.
- Types of Logic Table Input Pin : Year, date, week, time, universal switch, external input value, scene status, sequence status, external universal switch status, channel status, panel status, security device etc.
- Logic Relations : AND, OR, NAND, NOR.
- Built-in Real-time clock.
- Online update via Buspro

Important Notes

- Buspro cable – CAT5E or dedicated HDL Buspro/KNX cable
- Buspro connection – Series connection (hand-in-hand recommended)

Product Information

Dimensions - See Figure 2 - 3

Components - See Figure 4

1. Module Indicator: The indicator flashes when the module is working properly. Keep pressing the button for 3 seconds, the address of the module can be read and modified via HDL Buspro Setup Tool.

Installation - See Figure 5 - 7

- Step 1. Fix the DIN rail with screws.
- Step 2. Buckle the bottom cap of the Logic Automation Module on the edge of the DIN rail.
- Step 3. Press the device on the DIN rail, slide it and fix it up until an appropriate position is adjusted.

Safety Precautions

- The installation and commissioning of the device must be carried out by HDL or the organization designated by HDL. For planning and construction of electric installations, the relevant guidelines, regulations and standards of the respective country are to be considered.
- The device should be installed with Din rail in DB box. HDL does not take responsibility for all the consequences caused by installation and wire connection that are not in accordance with this document.
- Please do not privately disassemble the device or change components, otherwise it may cause mechanical failure, electric shock, fire or body injury.
- Please resort to our customer service department or designated agencies for maintenance service. The warranty is not applicable for the product fault caused by private disassembly.

Package Contents

HDL-MCLog.431*1 / Datasheet*1 / Label*5 / Buspro connector*1

产品概述

逻辑控制模块（见图1）可对一系列的目标集中控制。该模块可设置12个逻辑组，并且共有240个逻辑块。逻辑条件可实现日期和定时信息输入、通用开关状态和外部输入。

功能说明

- 可设计每天的工作事件。
- 可设定12个逻辑组；每个逻辑组可设置20个逻辑表，表与表之间可以连接，即一个逻辑表的输出可作为另外一个表的输入引脚。
- 每个逻辑表可设置4个输入引脚条件，20个输出目标。
- 逻辑表输入引脚类型：年类型，日期类型，星期类型，时间类型，通用开关，外部输入值，设备场景状态，设备序列状态，外部通用开关状态，设备回路状态，面板状态，消防设备等。
- 逻辑关系：与、或、与非、或非。
- 内置实时时钟。
- 支持总线升级。

注意事项

- 总线 - CAT5E或者HDL Buspro/KNX 专用电缆。
- 总线连接 - 推荐手拉手的连接方式。

产品信息

产品尺寸 - 见图 2 - 3

部件 - 见图 4

1. 模块指示灯, 正常工作时闪烁, 按该键约3秒, 则可以在HDL Buspro软件中读取和修改本模块地址

设备的安装 - 见图 5-7

- 步骤1. 用螺丝固定丁导轨。
- 步骤2. 将逻辑控制模块底盖的一端扣上导轨的边上。
- 步骤3. 将整个模块压到导轨上, 并滑动模块直至调整到合适的位置则固定下来。

安全警告

- 本设备的安装及调试工作必须由我司或我司指派的机构进行。其电气安装的计划 and 施工, 必须遵守当地相关的安全标准和法规。
- 该产品需以丁轨安装方式, 安装在电箱里, 未按照本说明书进行安装及接线方式的, 所造成的一切后果皆与我司无关。
- 请不要私自拆装或更换零部件, 否则可能会导致机械故障、电击、火灾或身体伤害。
- 请将一切维修工作交由我司售后部门或指定机构进行, 本产品的保修范围不适用于因用户拆卸产品而导致的故障。

装箱清单

HDL-MCLog.431*1 / 说明书*1 / 标签纸*5 / 总线接线端子*1



Figure 5 / 图 5

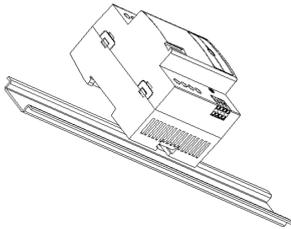


Figure 6 / 图 6

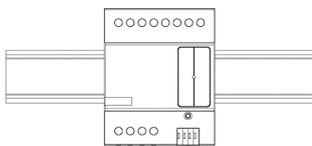


Figure 7 / 图 7

Figure 5 - 7. Installation
图 5 - 7. 安装图

EN

Technical Data

Electrical Parameters

Working voltage	15~30V DC
Working current	15mA/24V DC
Communication	HDL Buspro

External Environment

Working temperature	-5°C~45°C
Working relative humidity	≤90%
Storage temperature	-20°C~60°C
Storage relative humidity	≤93%

Specifications

Dimensions	72mm×90mm×66mm
Net weight	122g
Housing material	Nylon, PC
Installation	35mm DIN rail installation (See Figure 5 - 7)
Protection rating (Compliant with EN 60529)	IP20

Name and Content of Hazardous Substances in Products

Components	Hazardous substances					
	Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Chromium VI (Cr (VI))	Poly-brominated biphenyls (PBB)	Poly-brominated diphenyl ethers (PBDE)
Plastic	o	o	o	o	o	o
Hardware	o	o	o	o	-	-
Screw	o	o	o	x	-	-
Solder	x	o	o	o	-	-
PCB	x	o	o	o	o	o
IC	o	o	o	o	x	x

The symbol "-" indicates that the hazardous substance is not contained.

The symbol "o" indicates that the content of the hazardous substances in all the homogeneous materials of the component is below the limit requirement specified in the Standard IEC62321-2015.

The symbol "x" indicates that the content of the hazardous substance in at least one of the homogeneous materials of the part exceeds the limit requirement specified in the Standard IEC62321-2015.

HDL Buspro Cable Guide

HDL Buspro	HDL Buspro/KNX	CAT5/CAT5E
DATA+	Yellow	Blue/Green
DATA-	White	Blue White/Green White
COM	Black	Brown White/Orange White
24V DC	Red	Brown/Orange

ZH

技术参数

电气参数

工作电压	15~30V DC
工作电流	15mA/24V DC
通信方式	HDL Buspro

外部环境

工作温度	-5°C~45°C
工作相对湿度	≤90%
储存温度	-20°C~60°C
储存相对湿度	≤93%

产品规格

尺寸	72mm×90mm×66mm
净重	122g
外壳材料	尼龙, PC
安装方式	35mm 丁导轨安装 (见图5-7)
防护级别 (符合 EN 60529)	IP20

产品中有害物质的名称及含量

部件名称	有害物质					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (Cr (VI))	多溴联苯 (PBB)	多溴二苯醚 (PBDE)
塑胶组件	o	o	o	o	o	o
五金铜件	o	o	o	o	-	-
螺丝组件	o	o	o	x	-	-
焊锡	x	o	o	o	-	-
PCB组件	x	o	o	o	o	o
IC	o	o	o	o	x	x

- : 表示不含有该有害物质。

o : 表示该有害物质在该部件所有均质材料中的含量均在IEC62321-2015规定的限量要求以下。

x : 表示该有害物质至少在该部件的某一均质材料中的含量超出IEC62321-2015规定的限量要求。

HDL Buspro 总线定义

HDL Buspro	HDL Buspro/KNX	CAT5/CAT5E
DATA+	黄	蓝/绿
DATA-	白	蓝白/绿白
COM	黑	棕白/橙白
24V DC	红	棕/橙

技术支持

服务热线 : 020-85820195

(工作时间 : 9:00-12:00, 13:00-18:00, 星期一至星期五)

邮箱 : kfb@hdlchina.com.cn

网址 : http://www.hdlchina.com

Technical support

E-mail: support@hdlchina.com.cn

Website: https://www.hdlautomation.com

©Copyright by HDL Automation Co., Ltd. All rights reserved.
Specifications subject to change without notice.